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| APPLICATION NUMBER | FILING or 371(c) DATE | GRP ART UNIT | FIL FEE REC'D | ATTY. DOCKET NO | TOT CLAIMS | IND CLAIMS |
|--------------------|--------------------------|--------------|---------------|-----------------|------------|------------|
| 10/599,261 | 09/25/2006 | 1724 | 1400 | 870-003-208 | 30 | 3 |

CONFIRMATION NO. 6358

CORRECTED FILING RECEIPT

4955

WARE FRESSOLA VAN DER SLUYS &
ADOLPHSON, LLP
BRADFORD GREEN, BUILDING 5
755 MAIN STREET, P O BOX 224
MONROE, CT06468

Date Mailed: 08/16/2007

Receipt is acknowledged of this non-provisional patent application. The application will be taken up for examination in due course. Applicant will be notified as to the results of the examination. Any correspondence concerning the application must include the following identification information: the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections

Applicant(s)

Walter Georg Angelis, St. Georgen, GERMANY;
Wolfgang Laufer, Aichhalden, GERMANY;
Siegfried Seidler, Villingen-Schwenningen,
GERMANY;

Power of Attorney: The patent practitioners associated with Customer Number 4955

Domestic Priority data as claimed by applicant

This application is a 371 of PCT/EP05/02832 03/17/2005

Foreign Applications

GERMANY 10 2004 016 400.2 03/26/2004

If Required, Foreign Filing License Granted: 04/02/2007

The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is
US10/599,261

Projected Publication Date: Not Applicable

Non-Publication Request: No

Early Publication Request: No

Title

Method & arrangement for cooling a substrate, particularly a semiconductor

Preliminary Class

210

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